THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	:	
Shinichiro ETO et al.	:	**Attn: APPLICATION BRANCH**
Serial No. NEW	:	Docket No. 2001_0469A
Filed April 19, 2001	:	

REAL-TIME OS SIMULATOR

PRELIMINARY AMENDMENT TO REDUCE PTO FILING FEE

Assistant Commissioner for Patents,
Washington, DC 20231

Sir:

Please amend the above-identified application as follows.

In the Claims:

Kindly amend claim 5 as follows.

5. (Amended) The real-time OS simulator according to claim 2, wherein said task switching instruction means provides the instruction to said task switching thread after said task switching thread is enabled to start processing.

Kindly add new claim 26 as follows.

26. (New) The real-time OS simulator according to claim 4, wherein said task switching instruction means provides the instruction to said task switching thread after said task switching thread is enabled to start processing.

REMARKS

The above claim amendments are presented in order to remove multiple claim dependency, so as to reduce the required filing fee.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "**Version with markings to show changes made**."

Respectfully submitted,

Shinichiro ETO et al.

By _____
Charles R. Watts
Registration No. 33,142
Attorney for Applicants

CRW/asd
Washington, D.C. 20006-1021
Telephone (202) 721-8200
Facsimile (202) 721-8250
April 19, 2001

3. The real-time OS simulator according to claim 2, wherein

in response to the instruction for switching the tasks, said task switching thread checks at predetermined intervals whether the preceding running task thread is suspended or not.

4. The real-time OS simulator according to claim 1, wherein

said task switching instruction means selects a task processing thread to run next, provides the instruction for switching the tasks to said task switching thread, and then sets the task processing thread that has issued said request in a waiting state, and

in response to the instruction, said task switching thread suspends a preceding running task processing thread, and then releases the selected task processing thread from the waiting state for resuming.

5. The real-time OS simulator according to claim 2 ~~or 4~~, wherein

said task switching instruction means provides the instruction to said task switching thread after said task switching thread is enabled to start processing.

6. The real-time OS simulator according to claim 1, wherein

said task switching instruction means provides the